

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application.

COMPLETE LISTING OF CLAIMS:

Claims 1-22 : (Canceled)

Claim 23 : (New) An integrated circuit, comprising: at least one microstrip line; at least one port; and a removable, reflection-free termination integrated on a chip, for terminating at least one of the microstrip line and the port.

Claim 24 : (New) The integrated circuit according to claim 23, in that the integrated circuit is a microwave monolithic integrated circuit.

Claim 25 : (New) The integrated circuit according to claim 23, in that the integrated circuit is a radio-frequency circuit.

Claim 26 : (New) The integrated circuit according to claim 23, in that the integrated circuit is a test circuit.

Claim 27 : (New) The integrated circuit according to claim 23, in that the at least one port is a coplanar line port.

Claim 28 : (New) The integrated circuit according to claim 23, and comprising at least one of an amplifier, a mixer, a coupler, and a power splitter.

Claim 29 : (New) The integrated circuit according to claim 23, in that there is a plurality of microstrip lines, ports and terminations, each of the microstrip lines and ports being terminated by a respective one of the terminations.

Claim 30 : (New) The integrated circuit according to claim 23, and comprising input Lange couplers on the chip, the couplers having at least one port terminated by the termination.

Claim 31 : (New) The integrated circuit according to claim 23, in that the termination is an absorbing resistor.

Claim 32 : (New) The integrated circuit according to claim 23, in that there is a plurality of reflection-free terminations arranged symmetrically with respect to radio-frequency signal lines.

Claim 33 : (New) A method of manufacturing an integrated circuit, comprising the steps of: integrating at least one microstrip line, at least one port, and at least one removable, reflection-free termination on a chip; and removing the termination from a prescribable selection of at least one of the microstrip line and the port.

Claim 34 : (New) The method according to claim 33, in that the integrating step is performed by integrating a plurality of microstrip lines, a plurality of ports, and a plurality of terminations on the chip, and by terminating each of the lines and the ports with a respective one of the terminations.

Claim 35 : (New) The method according to claim 34, in that the terminating step is performed by using absorbing resistors as the terminations.

Claim 36 : (New) The method according to claim 34, in that the terminating step is performed by selecting and optimizing a position and dimensions of the terminations to be reflection-free.

Claim 37 : (New) The method according to claim 34, and arranging the terminations symmetrically with respect to radio-frequency signal lines.

Claim 38 : (New) The method according to claim 34, in that the removing step is performed by a laser.

Claim 39 : (New) The method according to claim 34, in that the removing step is performed in response to requirements of measurement arrangements used for making contact with radio-frequency connections.

Claim 40 : (New) The method according to claim 34, in that, following removal of the terminations, performing the step of connecting the ports and microstrip lines to a measurement device.

Claim 41 : (New) The method according to claim 40, in that a radio-frequency connection is used as a connection to the measurement device.

Claim 42 : (New) The method according to claim 40, and the step of using the measurement device to test individual parts of the integrated circuit, individually on their own.

Claim 43 : (New) The method according to claim 34, in that the removal step stipulates properties of the integrated circuit.

Claim 44 : (New) The method according to claim 34, in that the removal step stipulates a suppressed sideband of a mixer.